

PRIOR ART

Figure 1a

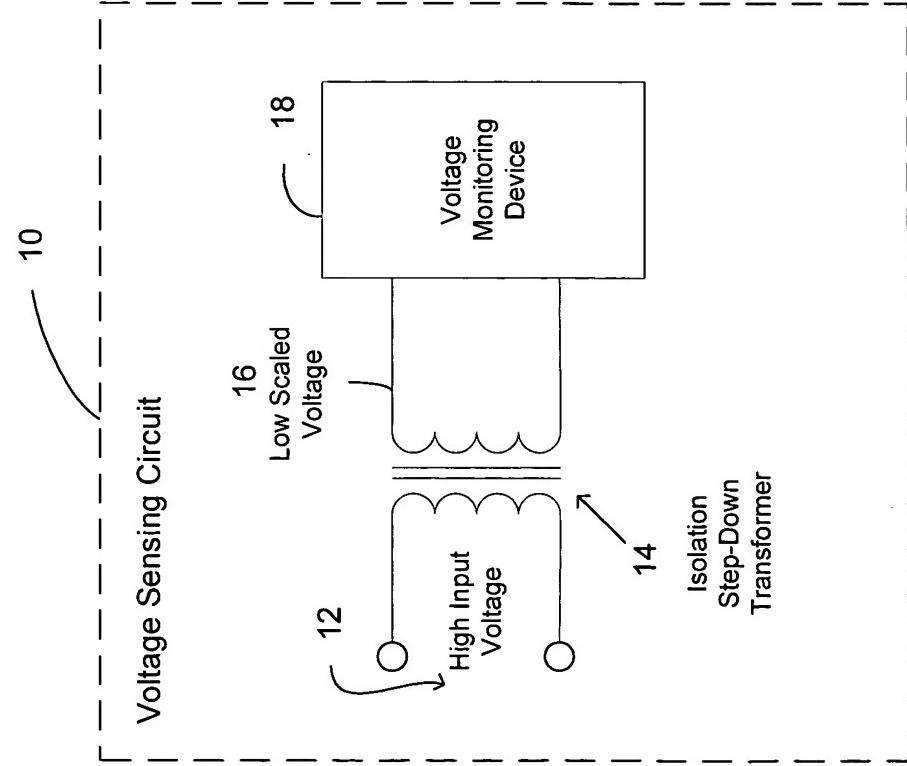
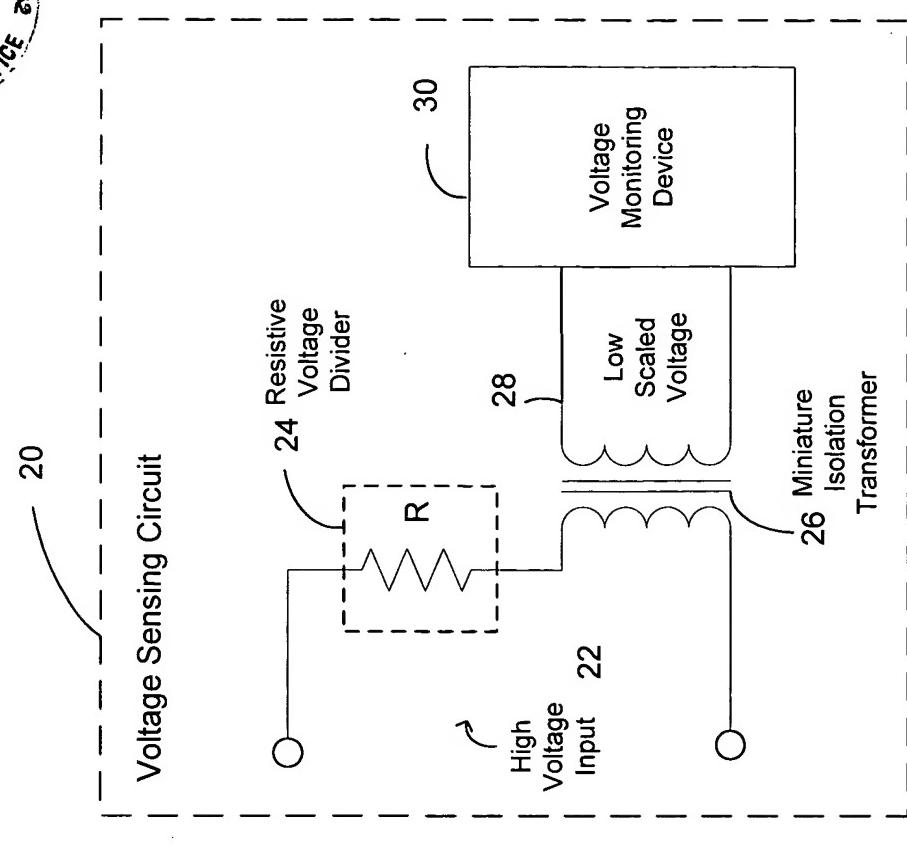


Figure 1b





Voltage Sensing Circuit

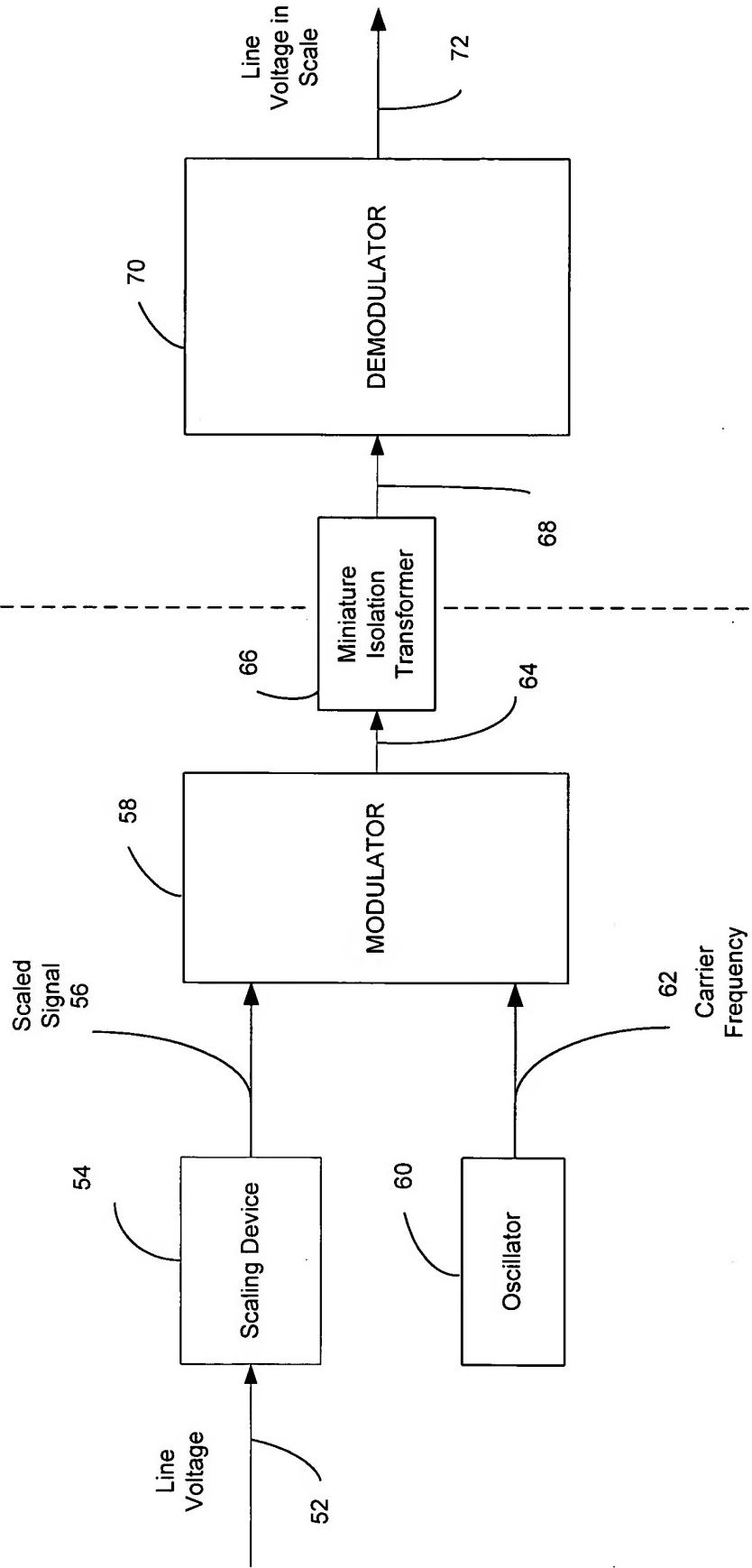


Figure 2

Figure 3

Voltage Sensing Circuit

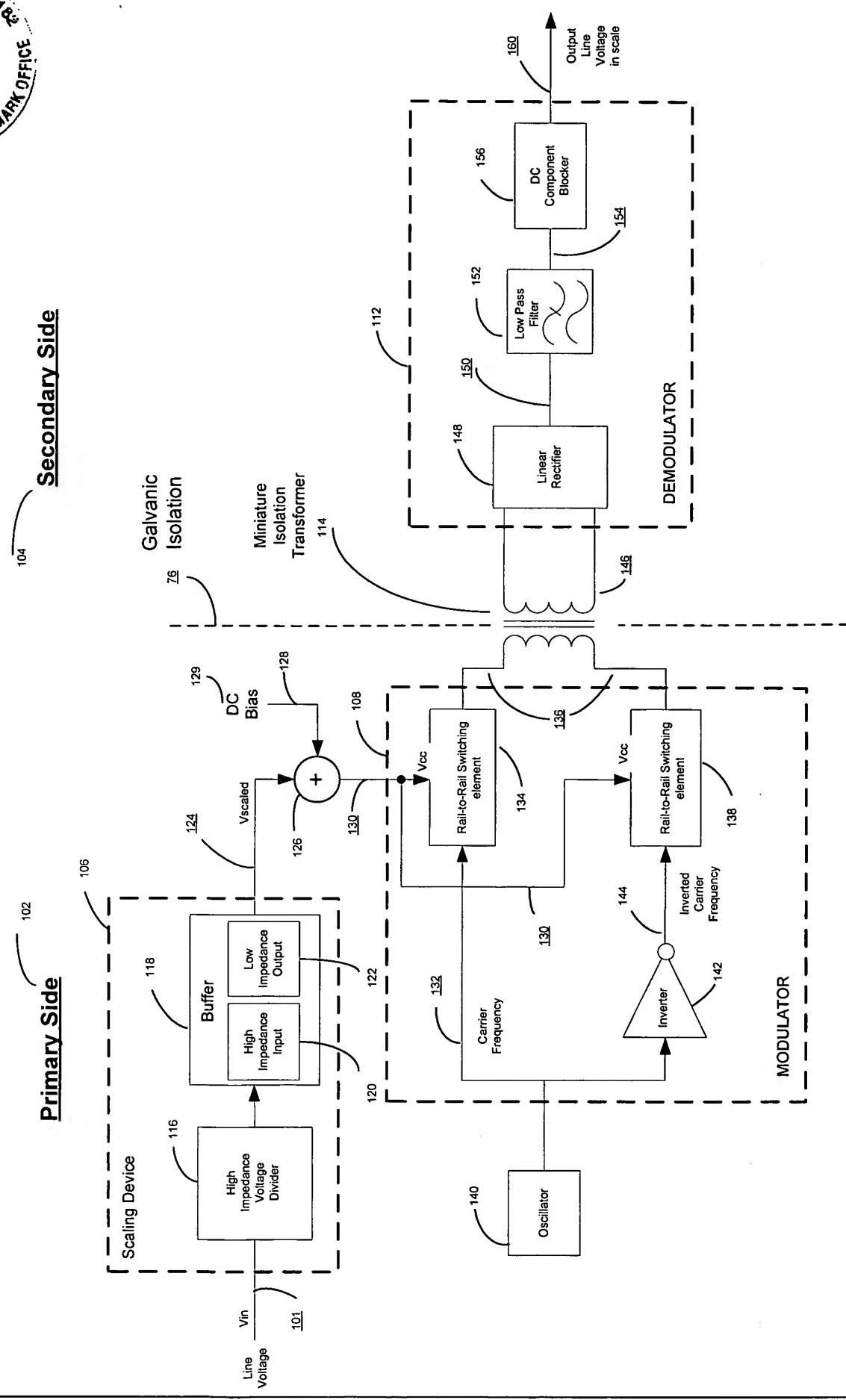




Fig. 4(a)

Line
Input
101

High Line voltage (example: 500 Vrms)

Fig. 4(b)

Scaled
Input
Signal
124

Scaled Low voltage (example: 5 Vrms)

Fig. 4(c)

Carrier
Frequency
132

Fig. 4(d)

Vcc
130

Scaled Low voltage + DC Bias

DC Bias (V1)

Fig. 4(e)

Modulator
and
Transformer
Output
136 and 146

Modulated Signal

DC Bias (V1)

DC Bias (V1)

Fig. 4(f)

Linear
Rectifier
Output
150

DC Bias (V1)

Fig. 4(g)

Low
Pass
Filter
Output
154

Filtered Signal

DC Bias (V1)

Fig. 4(h)

Output
Line
Voltage
in scale
160

Output Scaled Low voltage (Bias removed)

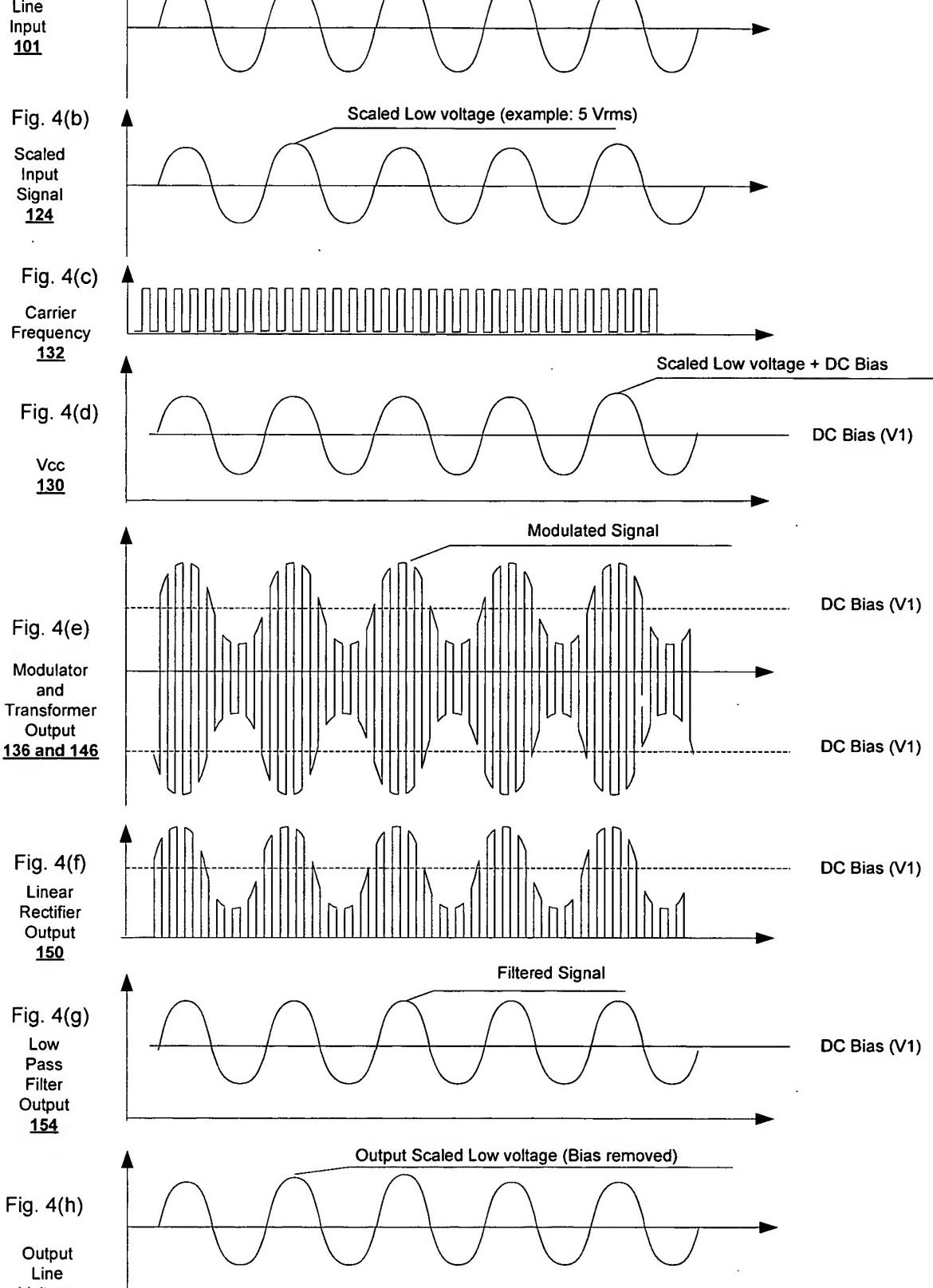


Figure 4 (a-h)



200

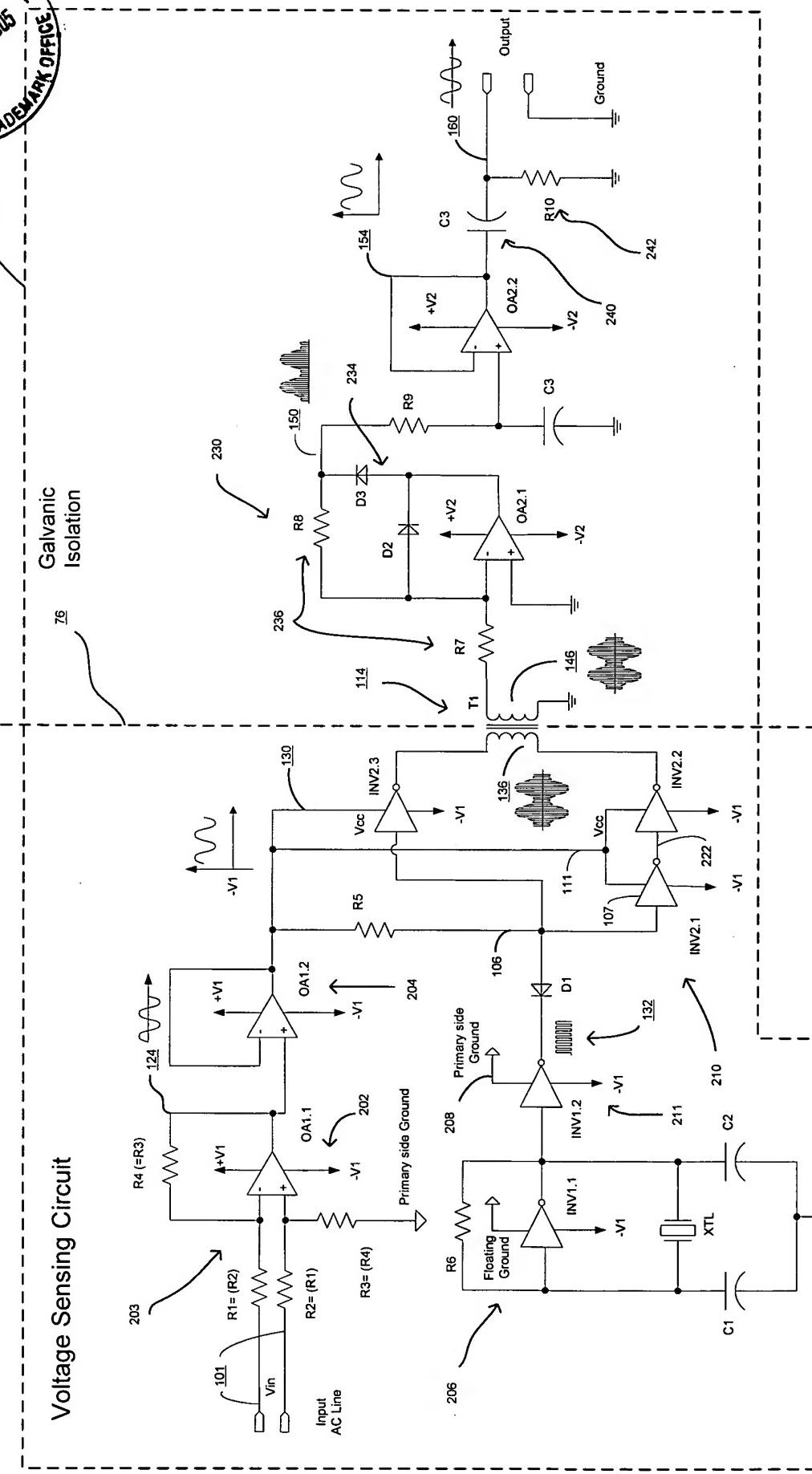


Figure 5

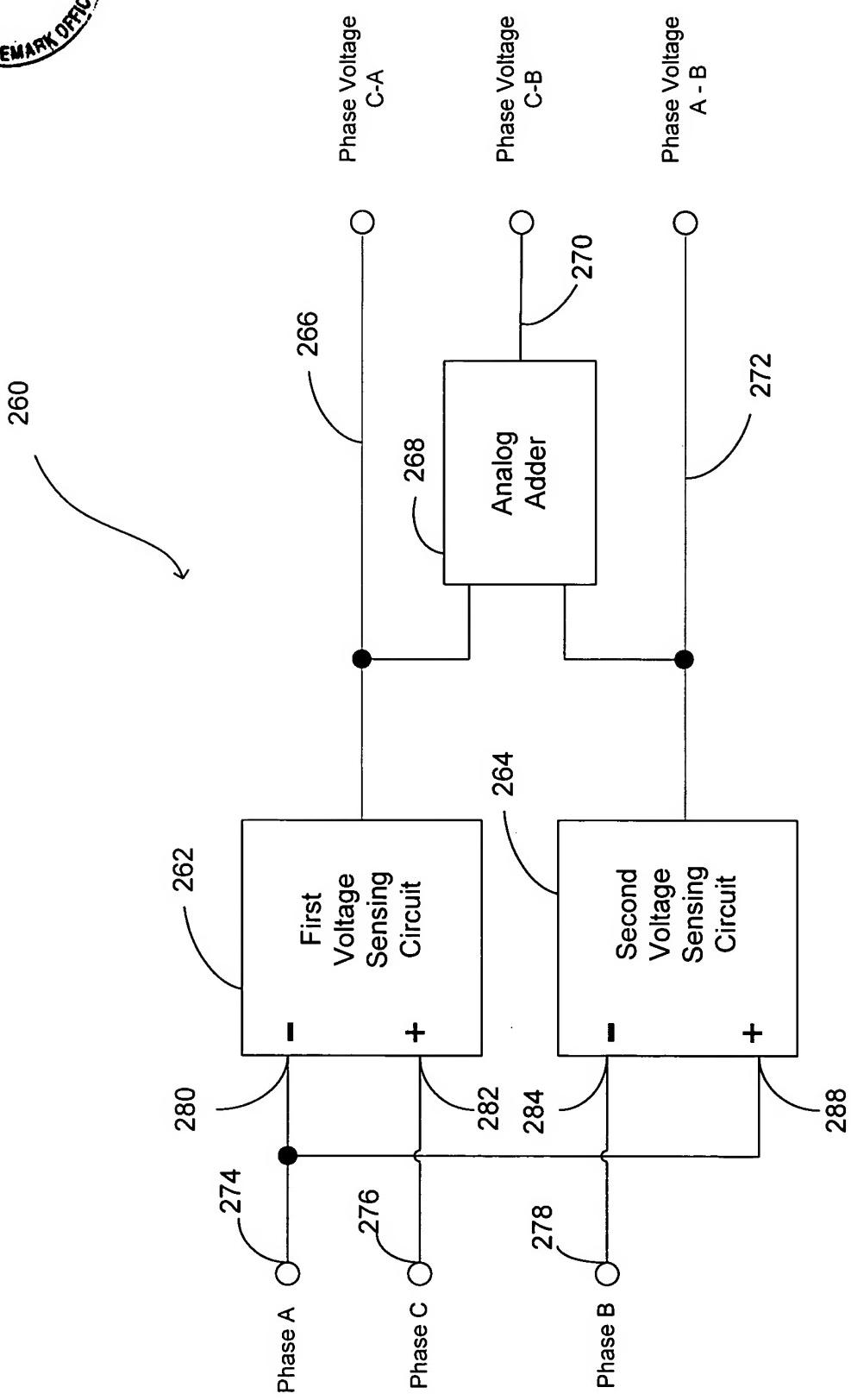


Figure 6

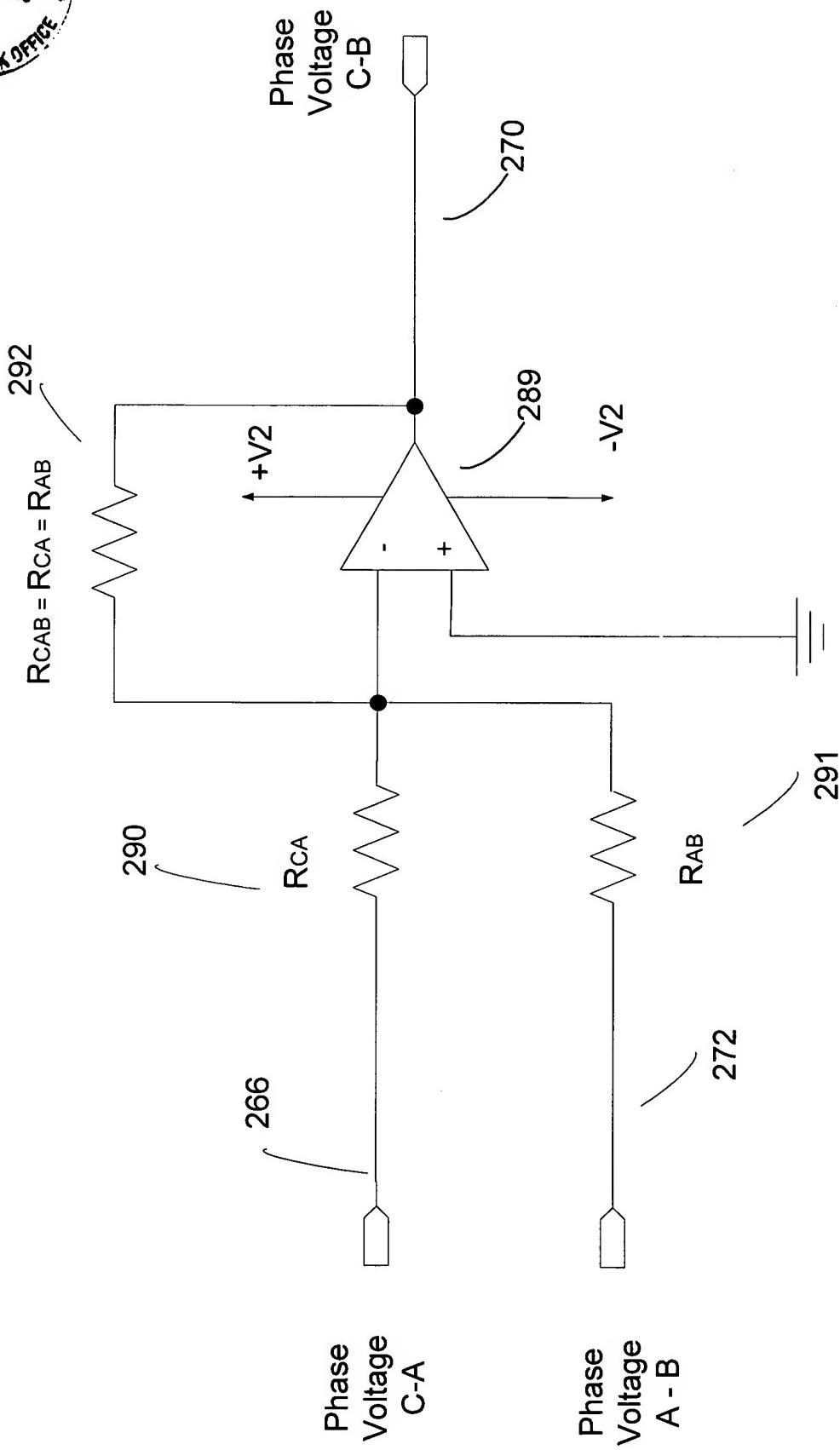


Figure 7